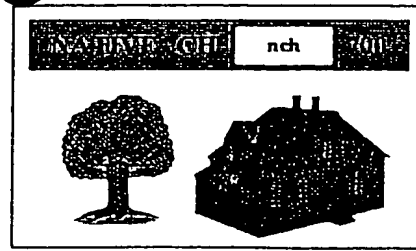
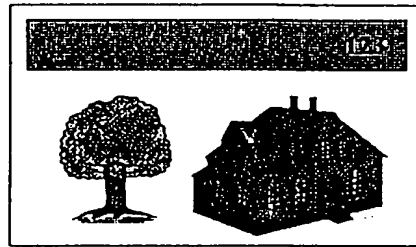


FIG. 1

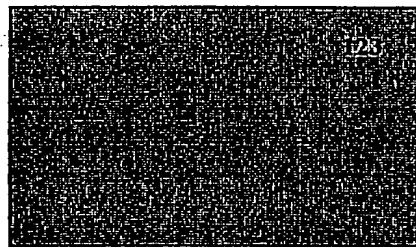
(A)



(B)



(C)



(D)



(E)

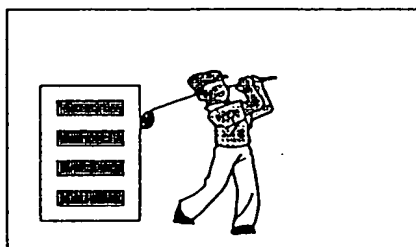


FIG. 2

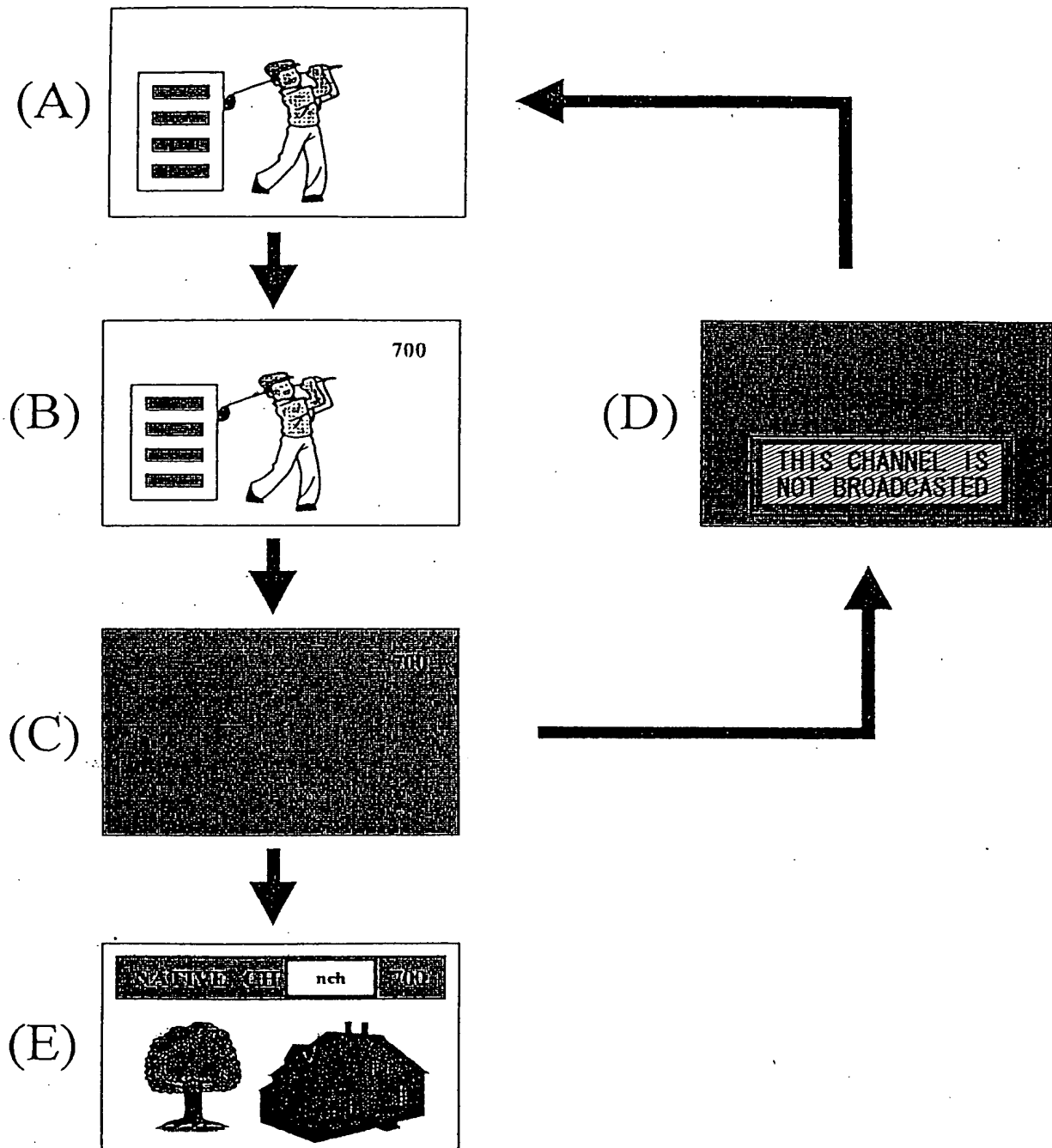


FIG. 3

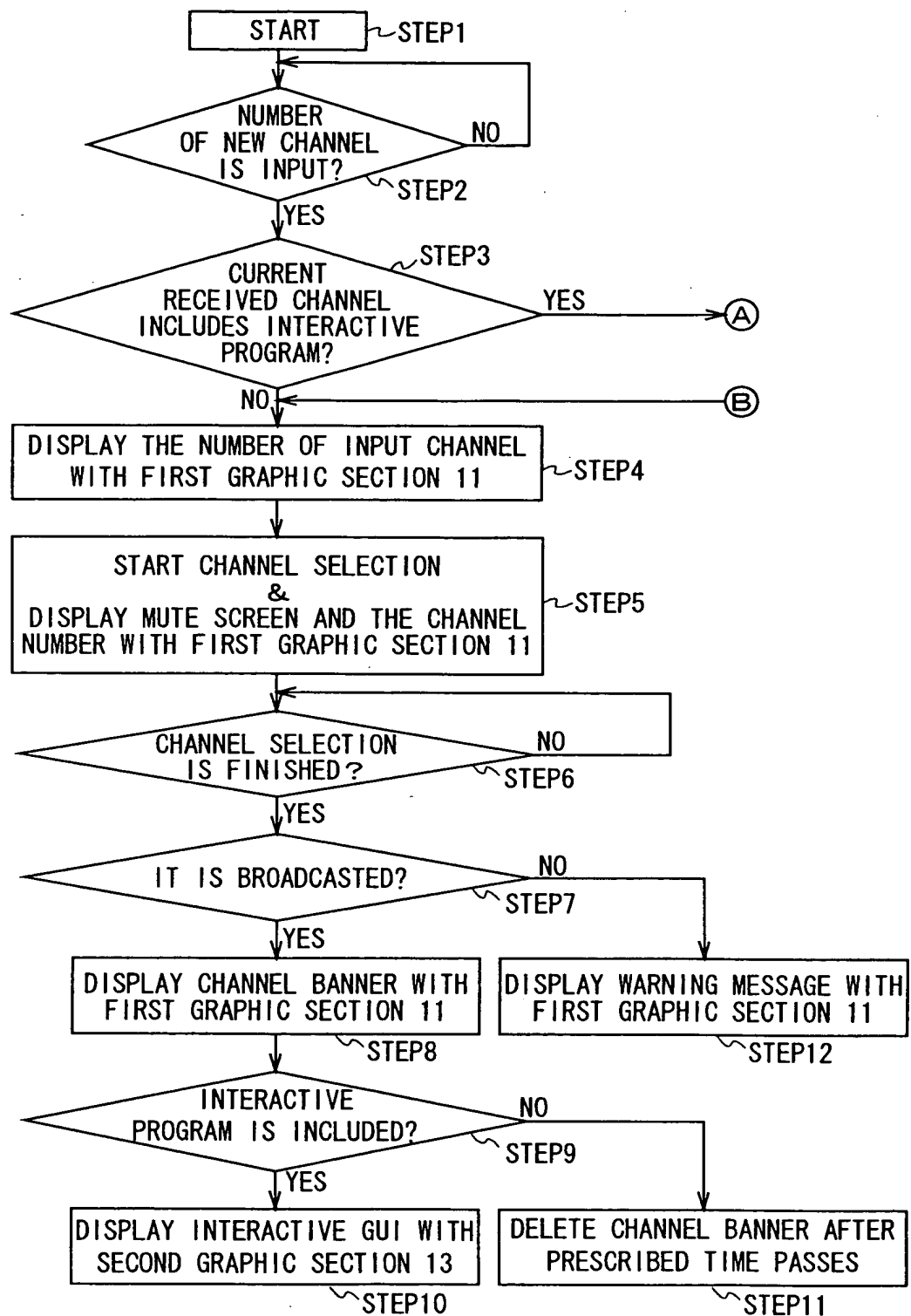


FIG. 4

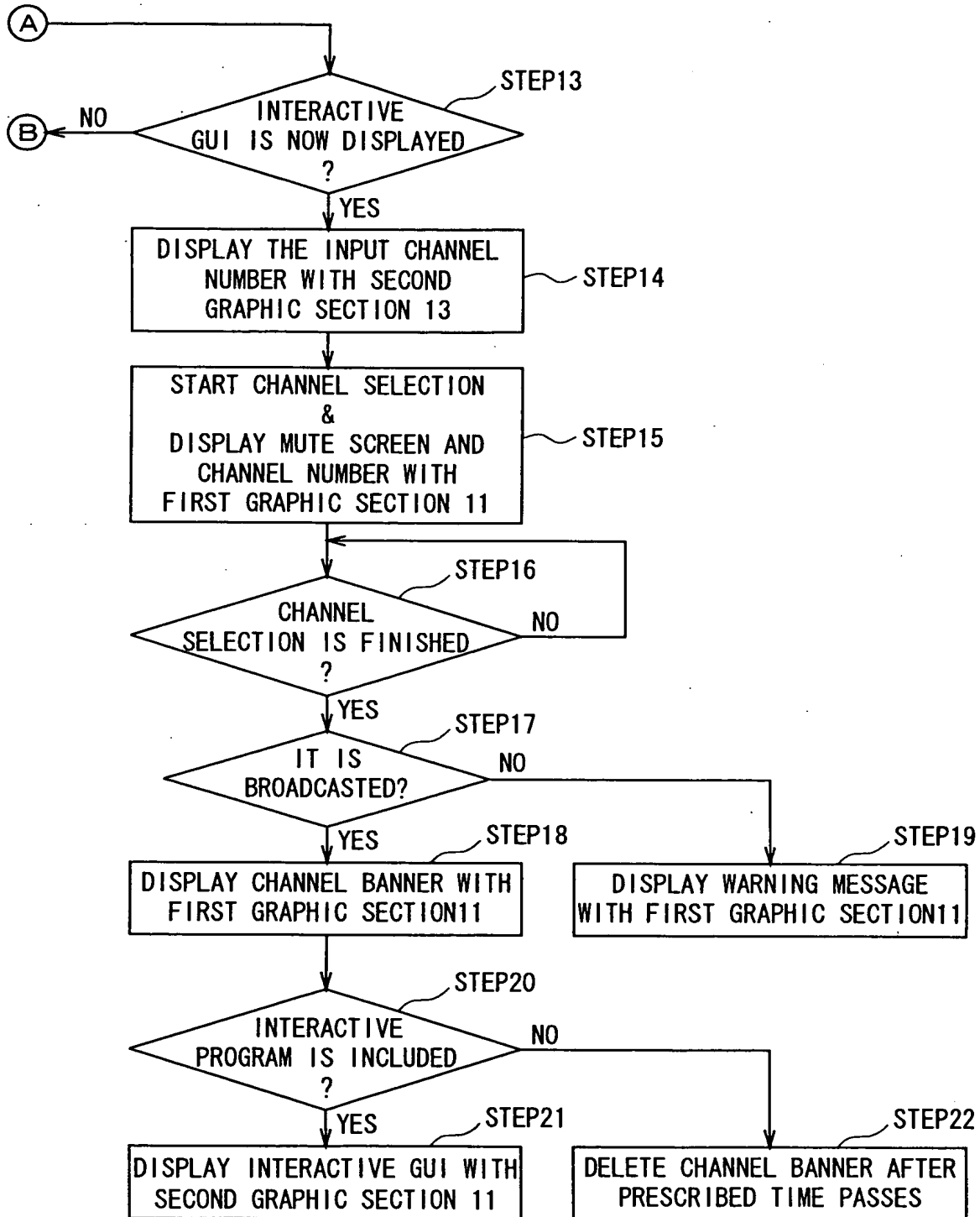


FIG. 5

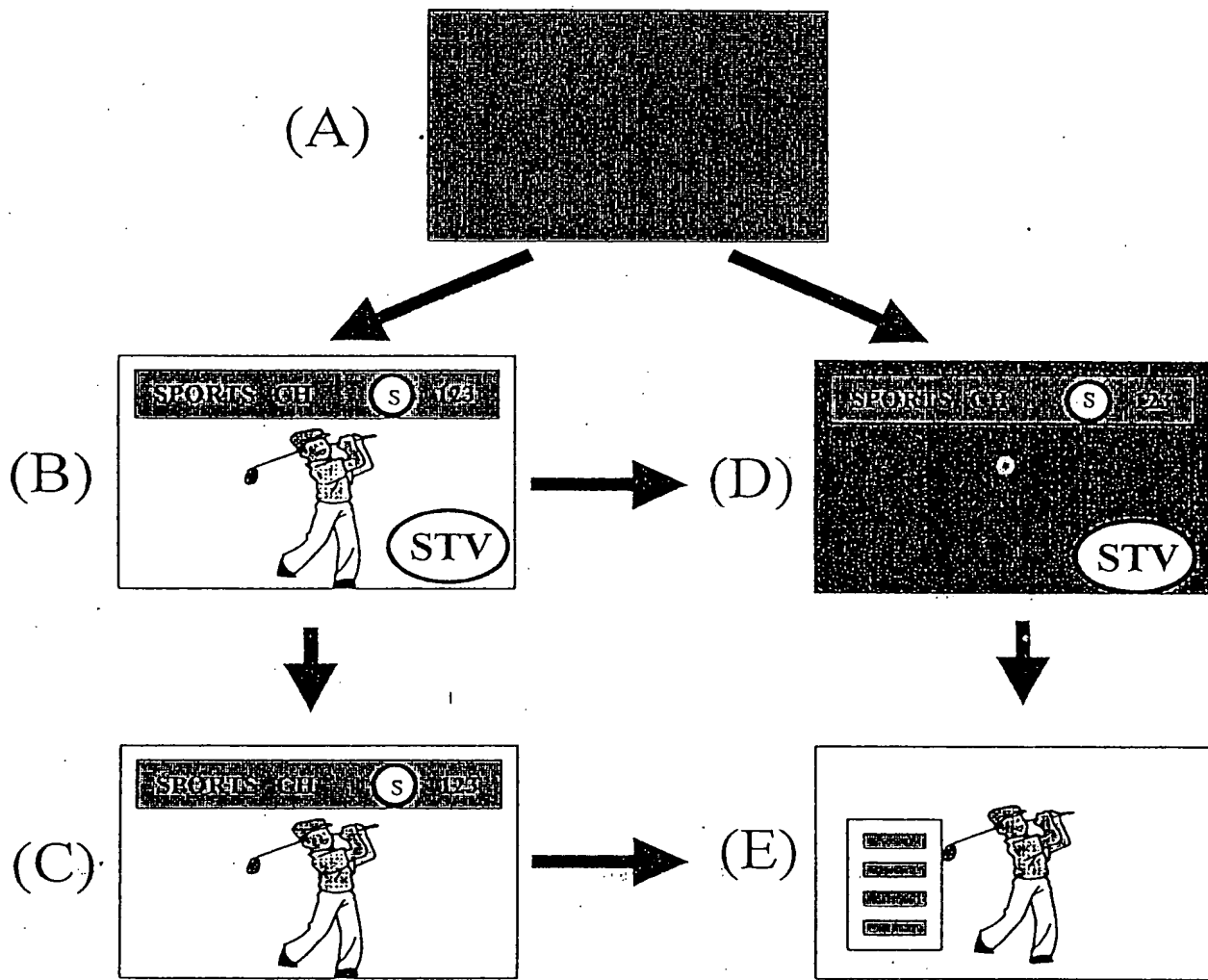


FIG. 6

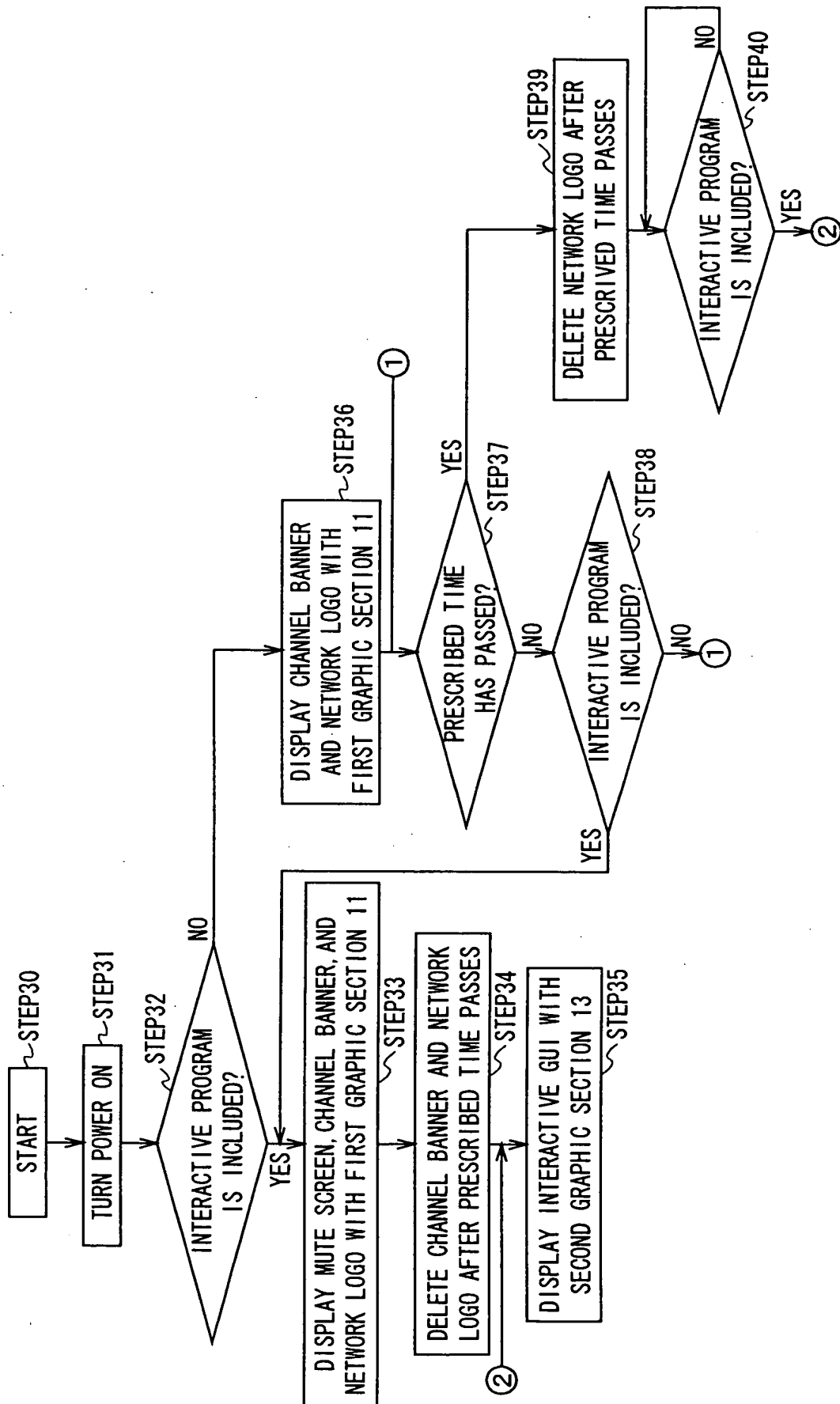


FIG. 7

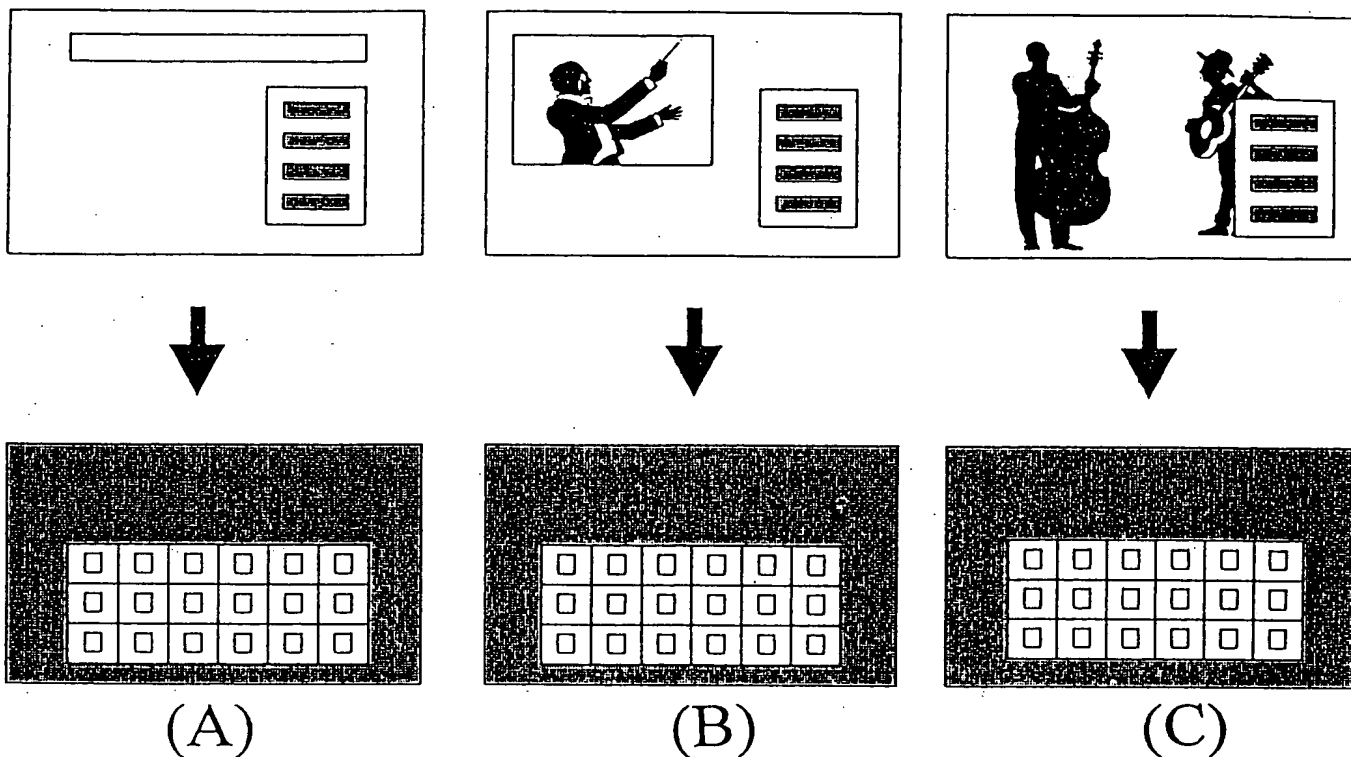


FIG. 8

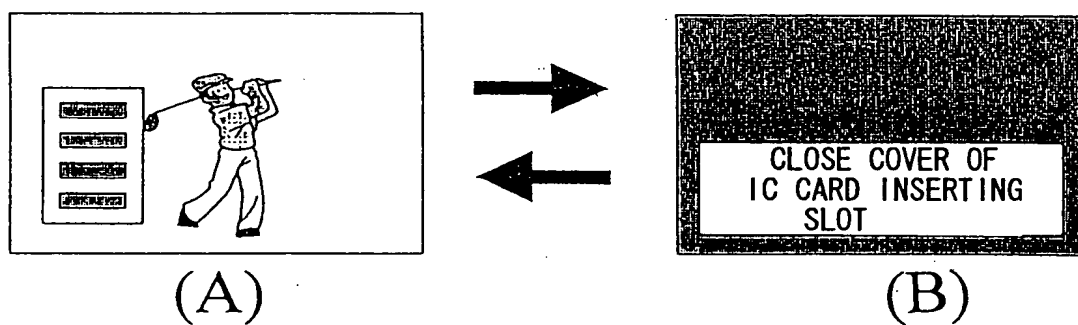


FIG. 10

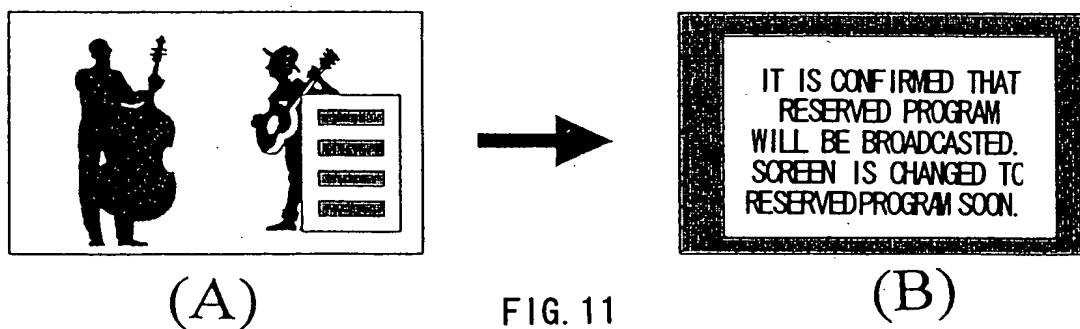


FIG. 11

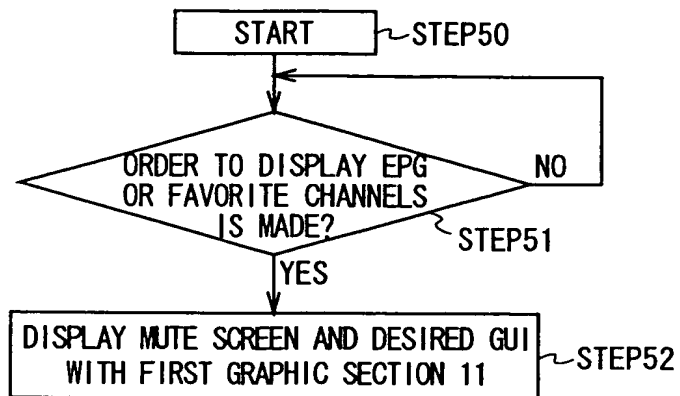


FIG. 9

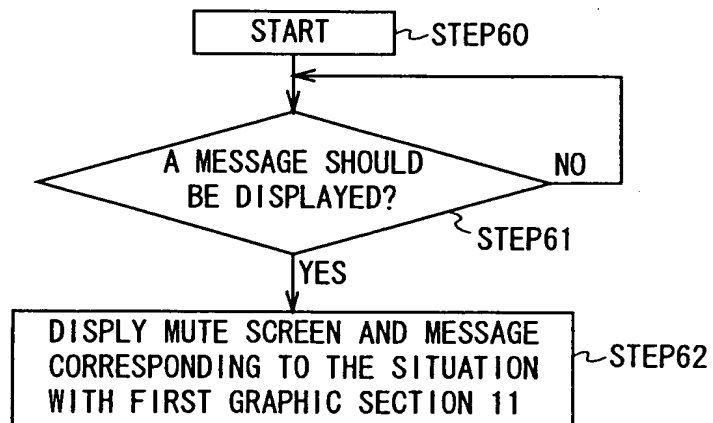


FIG. 12

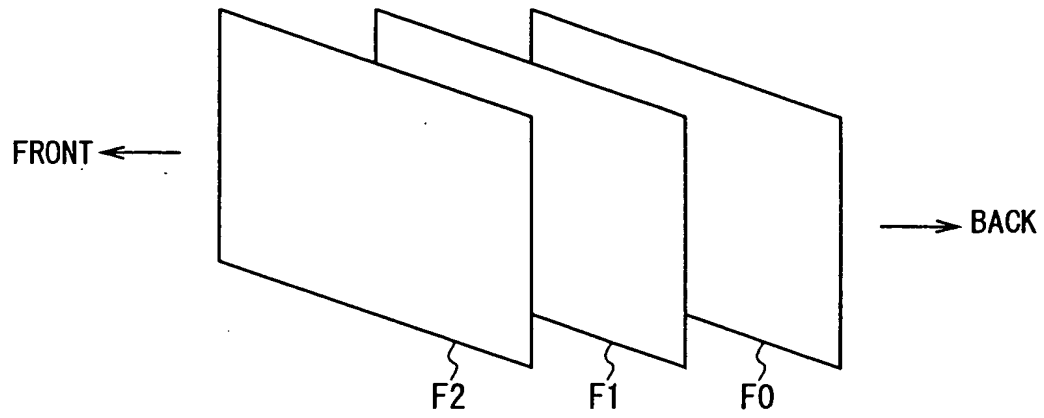


FIG. 13

Description of Reference Numerals

1...receiver, 2...antenna, 3...tuner, 4...input section,
5...bus, 6...CPU, 7...memory, 8...descramble section,
9...demultiplexer, 10...AV decoder, 11...first graphic section,
12...first work memory, 13...second graphic section, 14...second
work memory, 15...NTSC encoder